

* We have a channel with 4 KHz bandwidth. If we want to send data at 100 Kbps, what is the minimum SNR_{dB}?

⇒ Bandwidth (B) = 4 KHz
Bit rate (N) = 100 Kbps

We know, $N = B * \text{SNR}_{dB} / 3$

$$\Rightarrow 100 \times 10^3 = 4 \times 10^3 * \text{SNR}_{dB} / 3$$

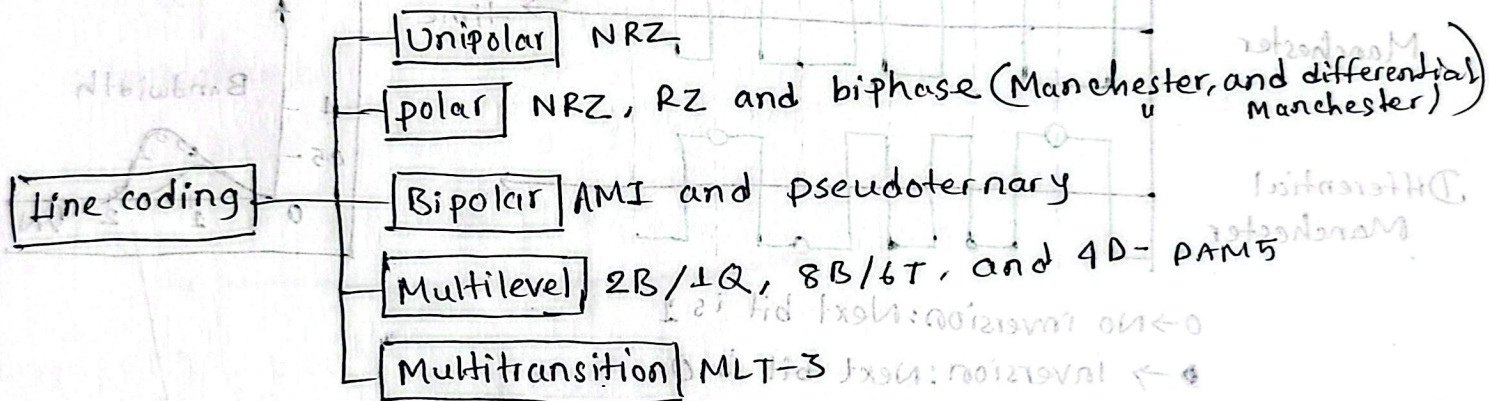
$$\Rightarrow 100 * 3 / 4 = \text{SNR}_{dB}$$

$$\Rightarrow \text{SNR}_{dB} = 75$$

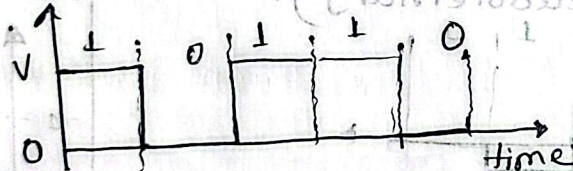
* Consider a noiseless channel with a bandwidth of 3000 Hz transmitting a signal with two signal levels. The maximum bit rate can be

$$\text{Bitrate} = 2 * 3000 * \log_2 2$$

$$= 6000 \text{ bps.}$$

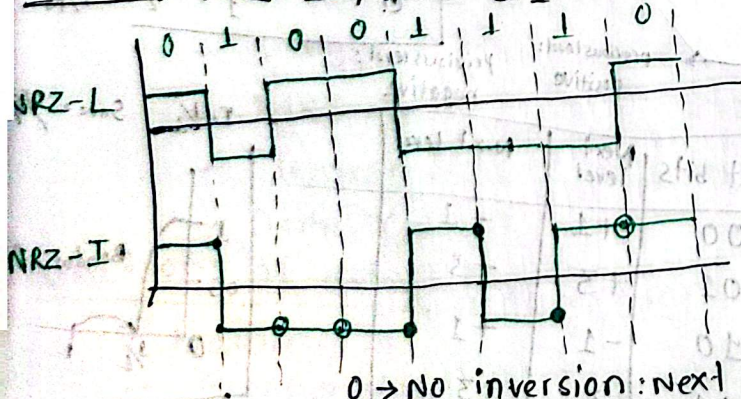


Unipolar NRZ : Amplitude



$$\frac{1}{2} V + \frac{1}{2} (0) = \frac{1}{2} V$$

Polar : NRZ-L, NRZ-I

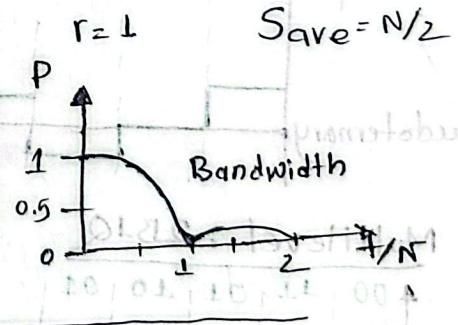


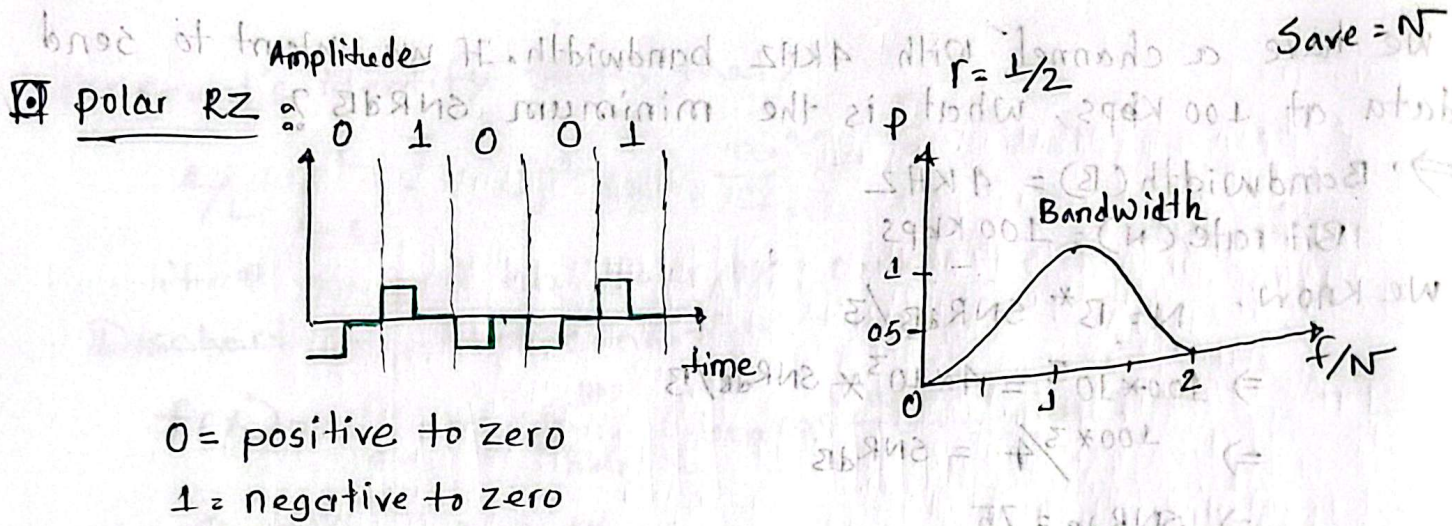
0 → No inversion: Next bit is 0
● → Inversion: Next bit is 1

NRZ-L: 0 = positive, 1 = negative.

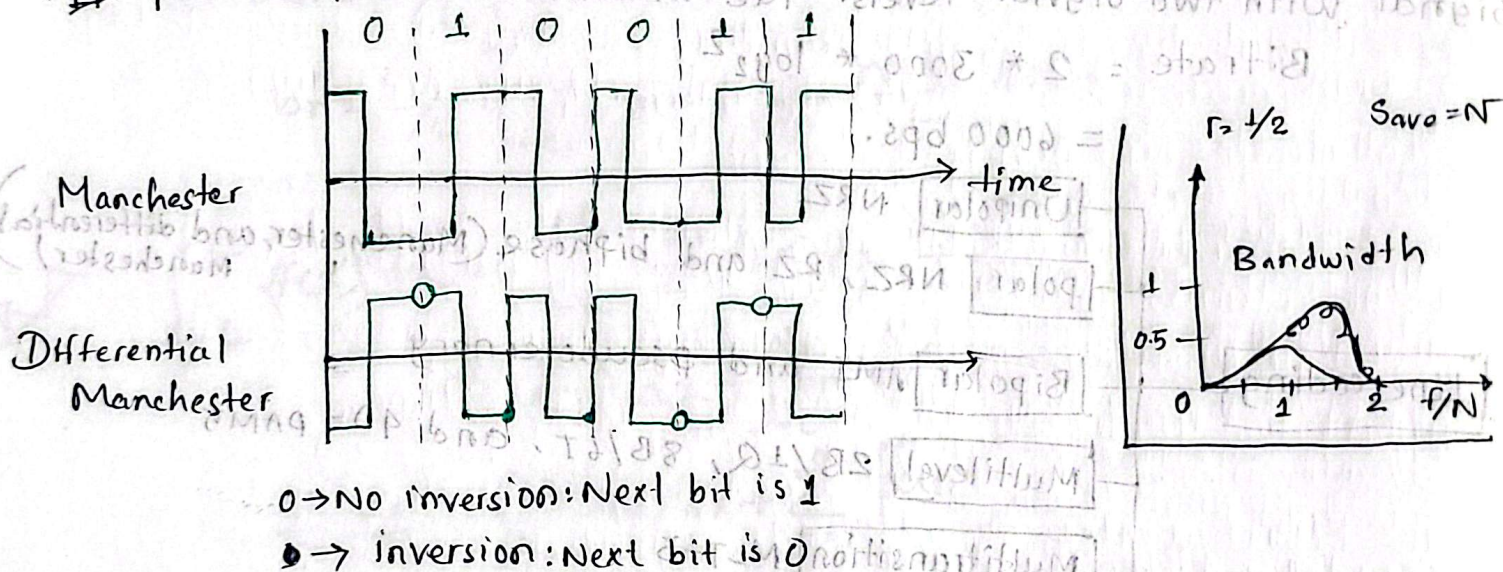
NRZ-I :

0 = no transition
1 = transition

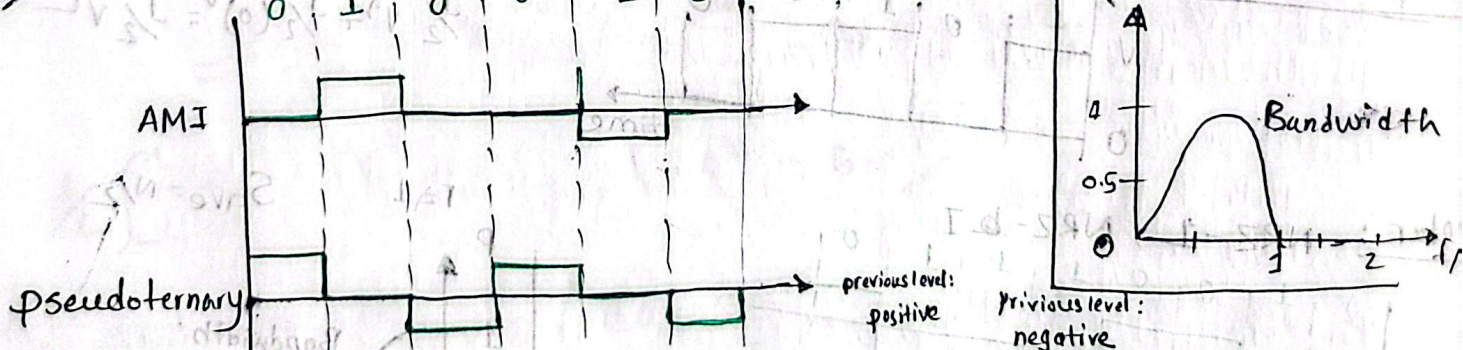




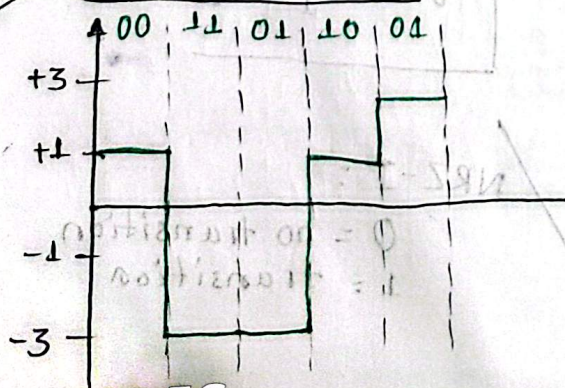
polar biphase: Manchester & differential manchester.



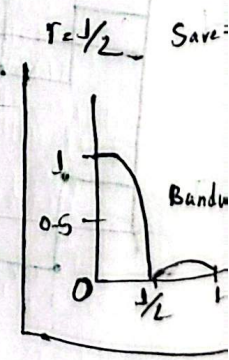
Bipolar (AMI and pseudoternary):



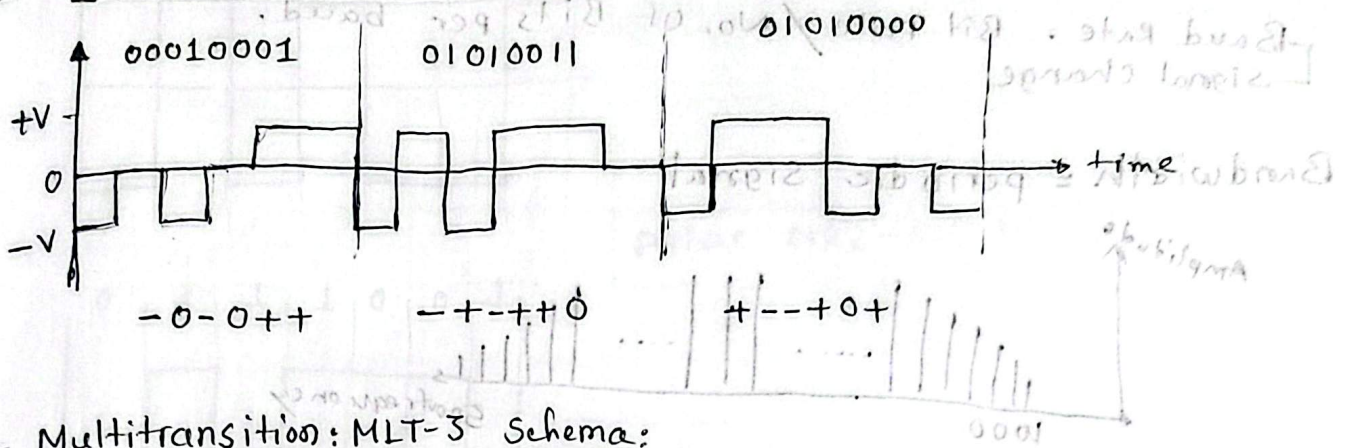
Multilevel: 2B1Q



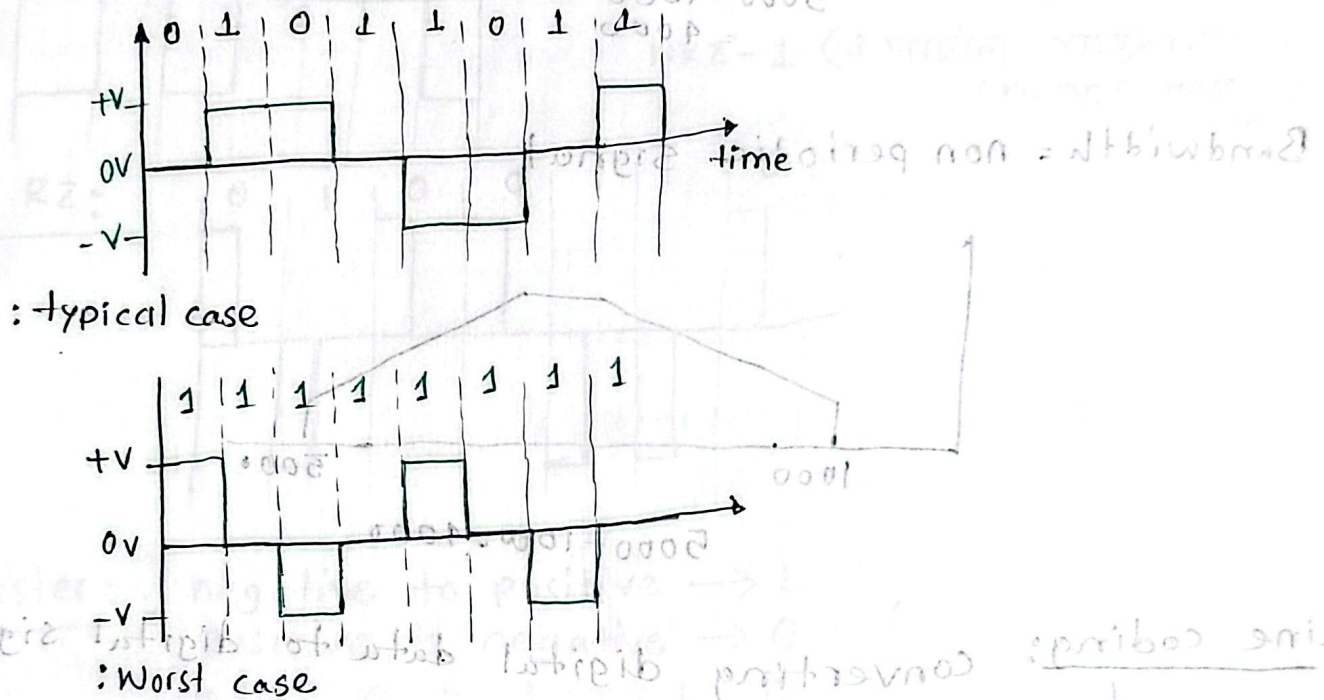
Next bits	Next level	Next level
00	+1	-1
01	+3	-3
10	-1	+1
11	-3	+3



Multilevel : 8B6T Schema:



Multitransition: MLT-3 Schema:



Functions of physical layer:

- Bit by bit transmission
- Encoding and decoding
- signal transmission
- Modulation and demodulation.
- Transmission modes.
- Data control

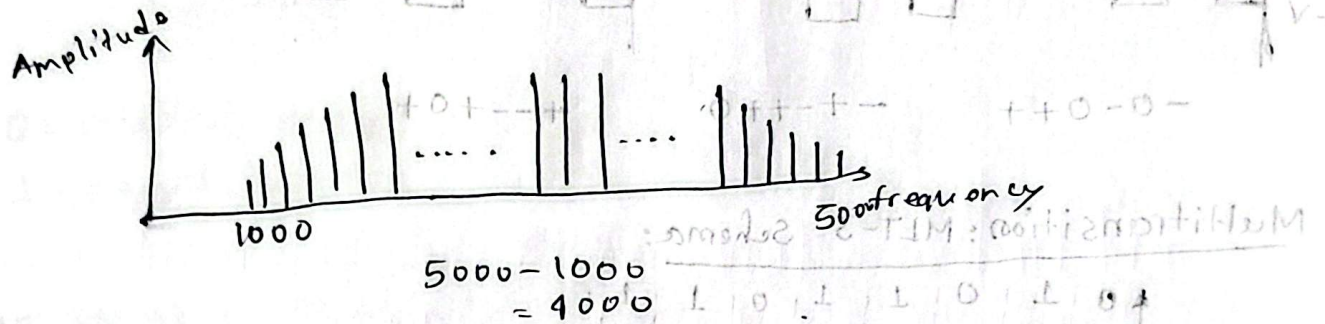
Functions of data link layer:

- Framing
- error detection - parity check
- error detection - checksum
- error detection - CRC
- error correction
- flow control
- addressing.

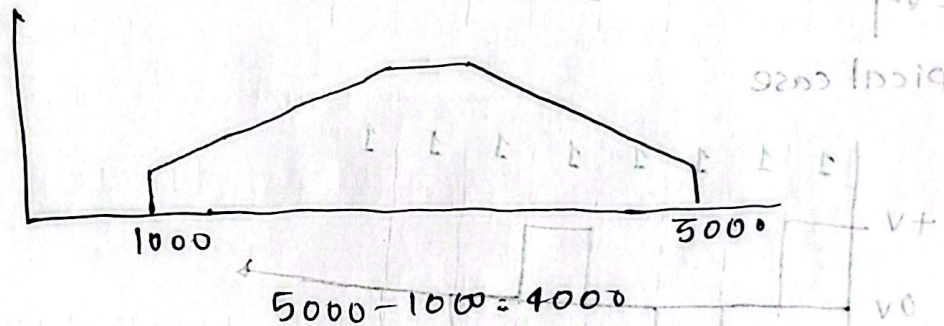
Bits — Bit rate = Baud Rate $\times$ no. of Bits per baud.

Baud Rate = Bit rate / No. of Bits per baud.
 [Signal change]

Bandwidth = periodic signal



Bandwidth = non periodic signal

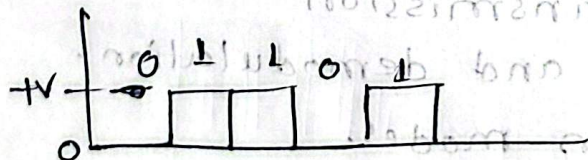


Line coding: converting digital data to digital signal

unipolar
 polar
 bipolar

three different type of line coding.

① Unipolar: 1 represent positive voltage and 0 represent 0 voltage.

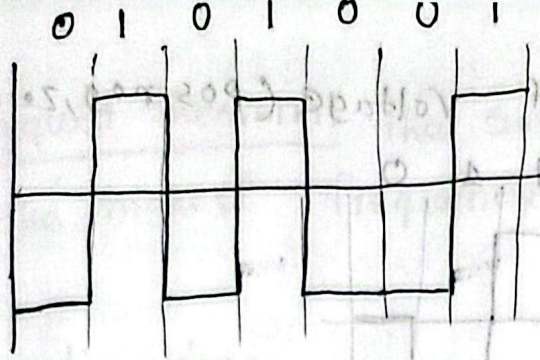


① polar:

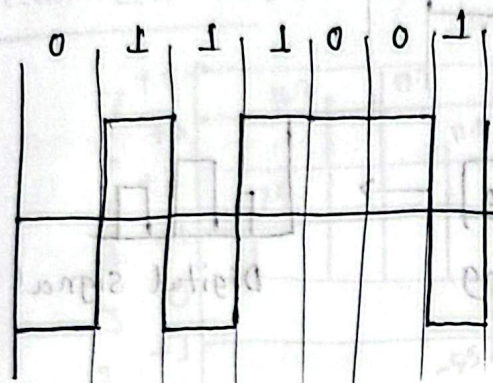
① polar NRZ: positive $\rightarrow$ 1
 negative $\rightarrow$ 0

types

NRZ-L
 NRZ-I

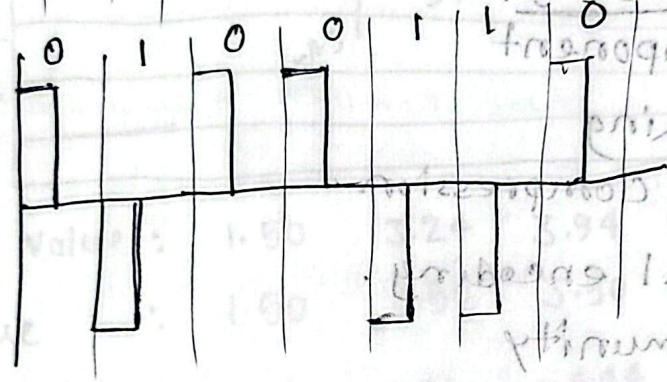


polar NRZ-L



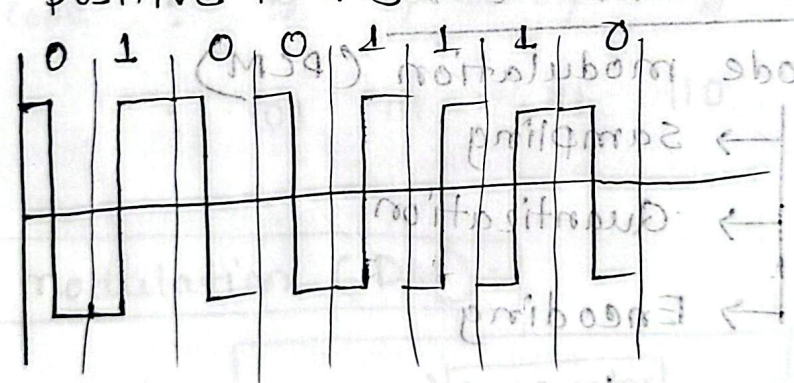
NRZ-1 (1 पाठले परिवर्तन change करा)

polar RZ:



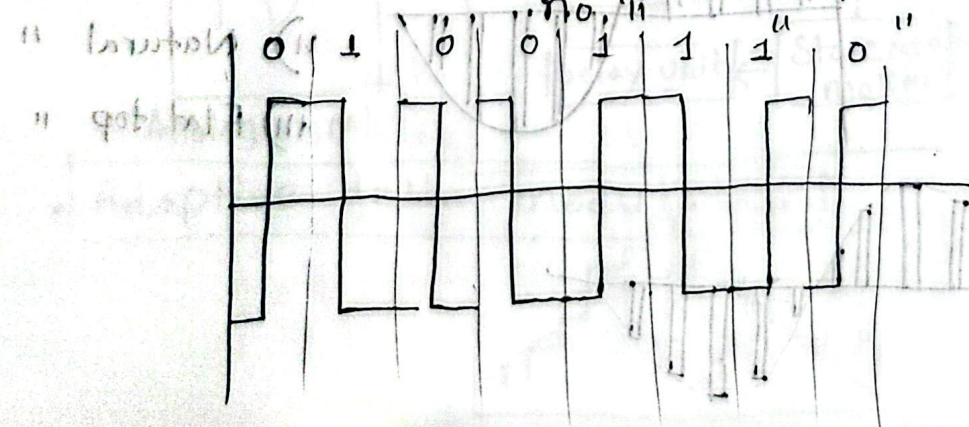
Manchester:

negative to positive $\rightarrow 1$
positive to negative $\rightarrow 0$

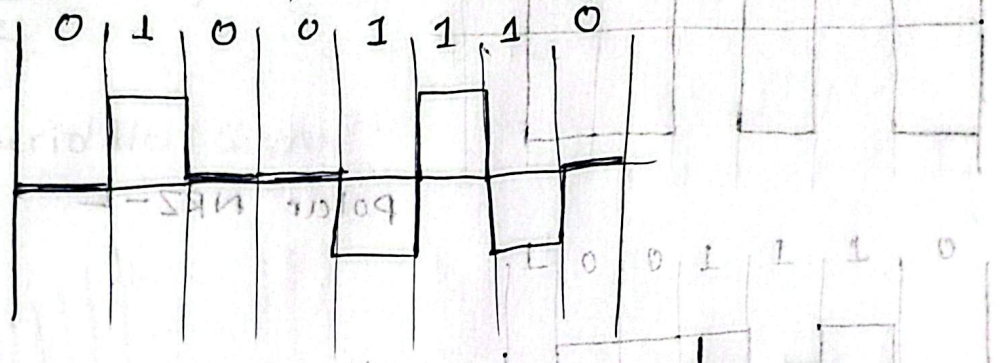


Differential Manchester:

transition is represented by $\rightarrow 0$
no transition $\rightarrow 1$

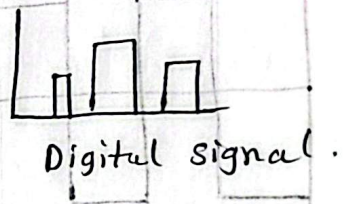


Bipolar encoding: three types of voltage (pos, neg, zero)



[0][1][1][0][1]

→ Line coding



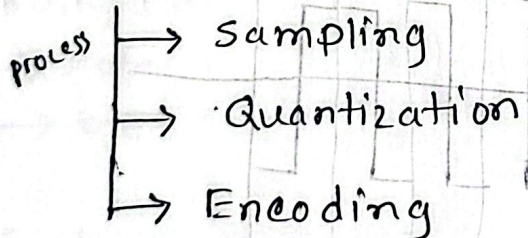
Sequence of bits → line coding

properties of line coding:

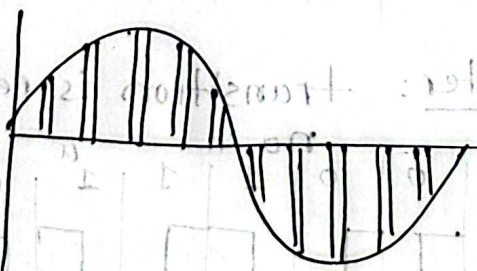
- No DC component
- self clocking
- Bandwidth compression
- Differential encoding.
- Noise immunity
- Minimum crosstalk.

Analog to digital conversion

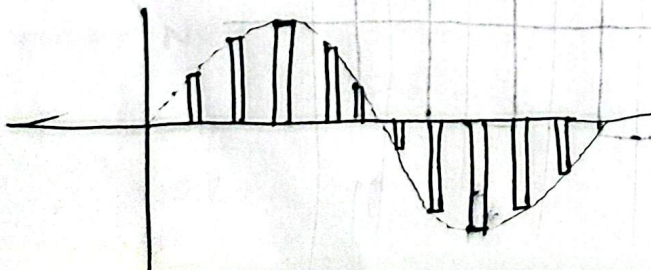
• pulse code modulation (PCM)



Sampling:

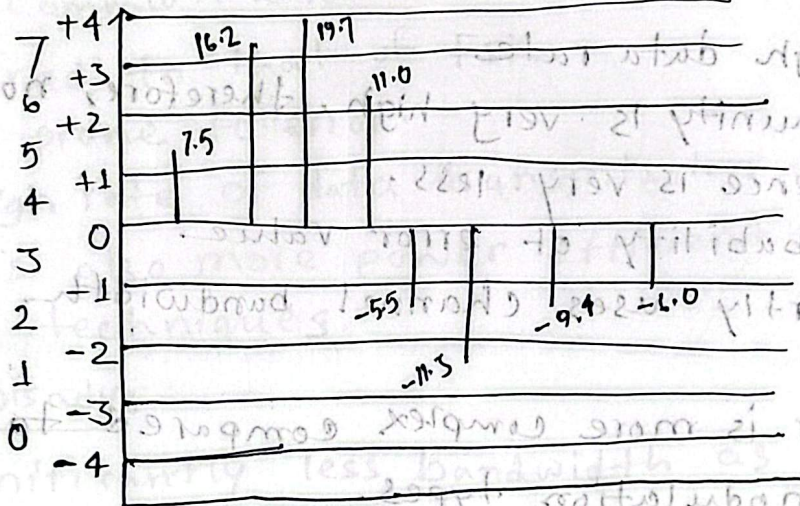


- i) Ideal
- ii) Natural "
- iii) Flat top "



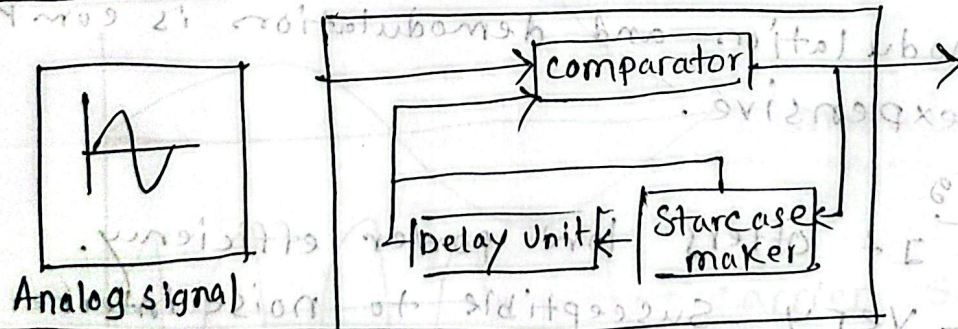
Nyquist theorem: the sampling rate must be at least 2 times the highest frequency contained in the signal.

Quantization:

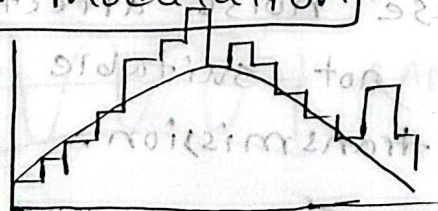


Normalized PAM value :	1.50	3.24	3.94	2.20	-1.10	2.26	-1.88	-1.20
Quantized value :	1.50	3.50	3.50	2.50	-1.50	-2.50	-1.50	-1.50
Normalized error :	0	0.26	-0.44	0.30	-0.40	-0.24	0.38	-0.30
Quantization code :	5	7	7	6	2	1	2	2
Encoded words :	101	111	111	110	010	001	010	010

Delta modulation (DM):



Adaptive delta modulation:



Quadrature Amplitude Modulation: (QAM)
define as a modulation technique that can be used to combine two amplitude modulated waves into a single channel to increase the channel bandwidth combination of (ASK and PSK).

Adv: - support high data rate

- Noise immunity is very high, therefore, noise & interference is very less.
- Low probability of error value.
- It expertly uses channel bandwidth.

Disadv:

- the receiver is more complex compared to receiver of other modulation types.

Amplitude Shift Keying (ASK):

Amplitude change but frequency and phase remains constant.

adv:

- it offers high bandwidth efficiency.
- Simple receiver design.
- Can be used to transmit digital data over optic fiber.
- modulation and demodulation is comparatively inexpensive.

Disadv:

- It offers low power efficiency.
- Very susceptible to noise interference because noise affects the amplitude.
- It is not suitable for high bit rate data transmission.

Phase Shift Keying (PSK):

conveys data by changing the phase of a constant frequency reference signal.

adv:

- it allows information to be transmitted in radio communication in a more efficient way as compared to that of FSK.
- less prone to error.
- A high rate of data transmission is achieved.
- It is also more power efficient as compared to other techniques.

Disadv:

- Significantly less bandwidth as compared to ASK.
- It is a type of non-coherent reference signal.
- extremely difficult algorithms are used in decoding the binary information which is transmitted during PSK.

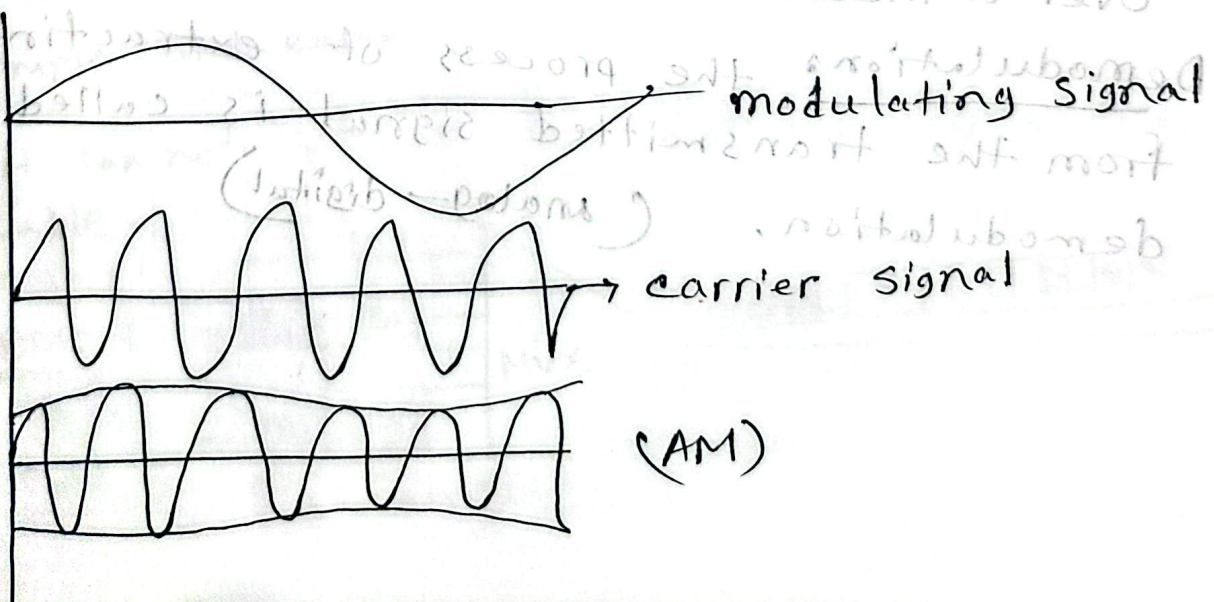
Analog to Analog conversion:

• Amplitude Modulation (AM)

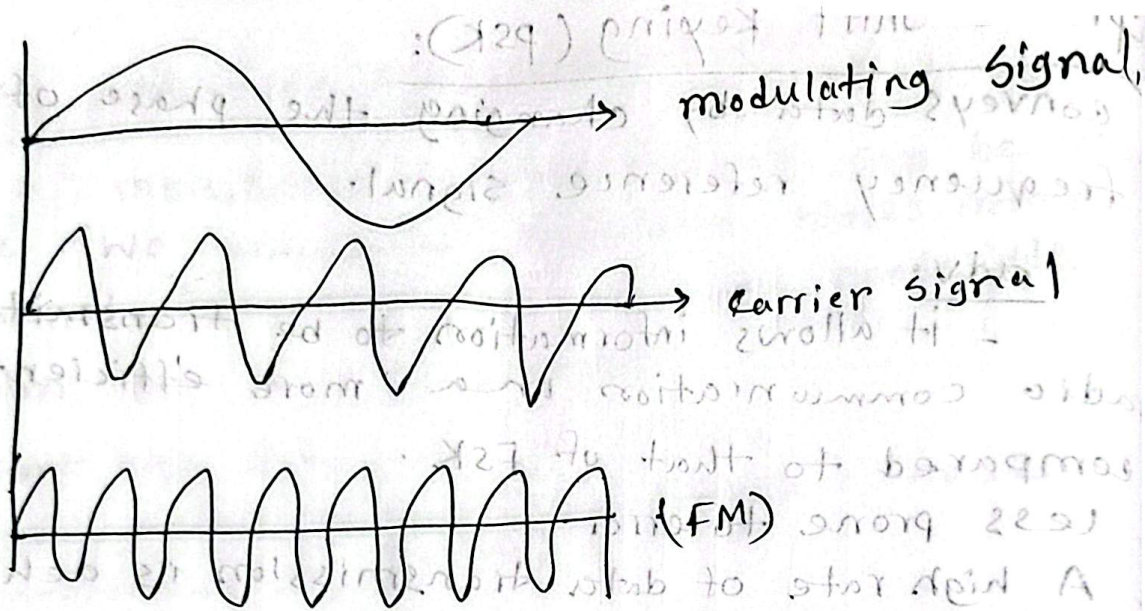
• Frequency " (FM)

• phase " (PM)

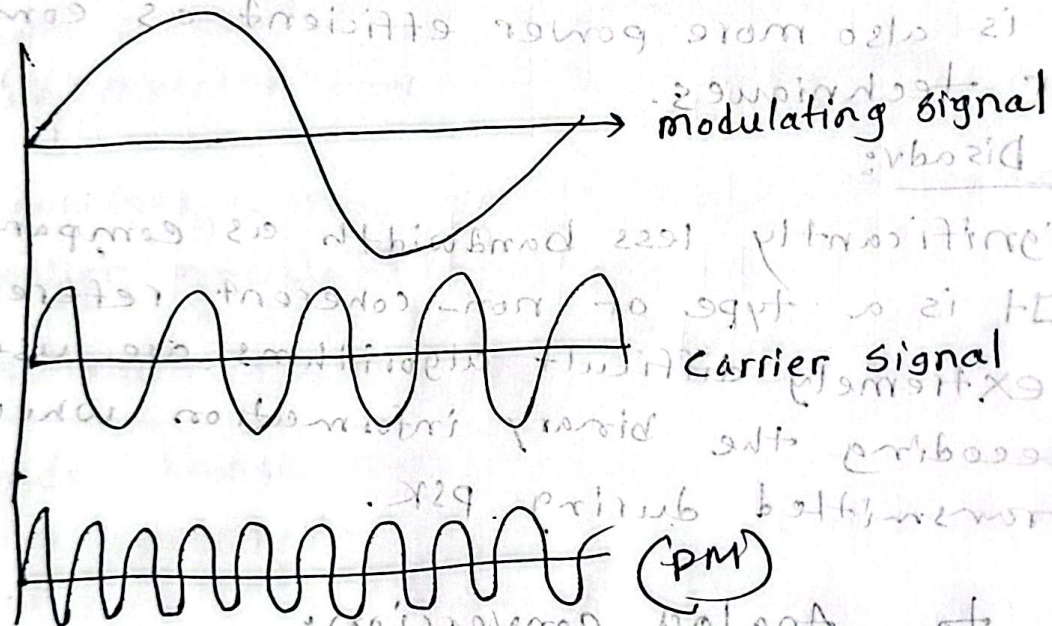
AM:



FM:

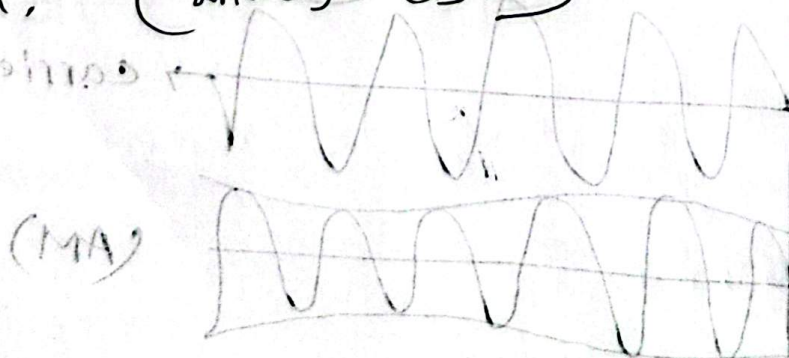


PM:

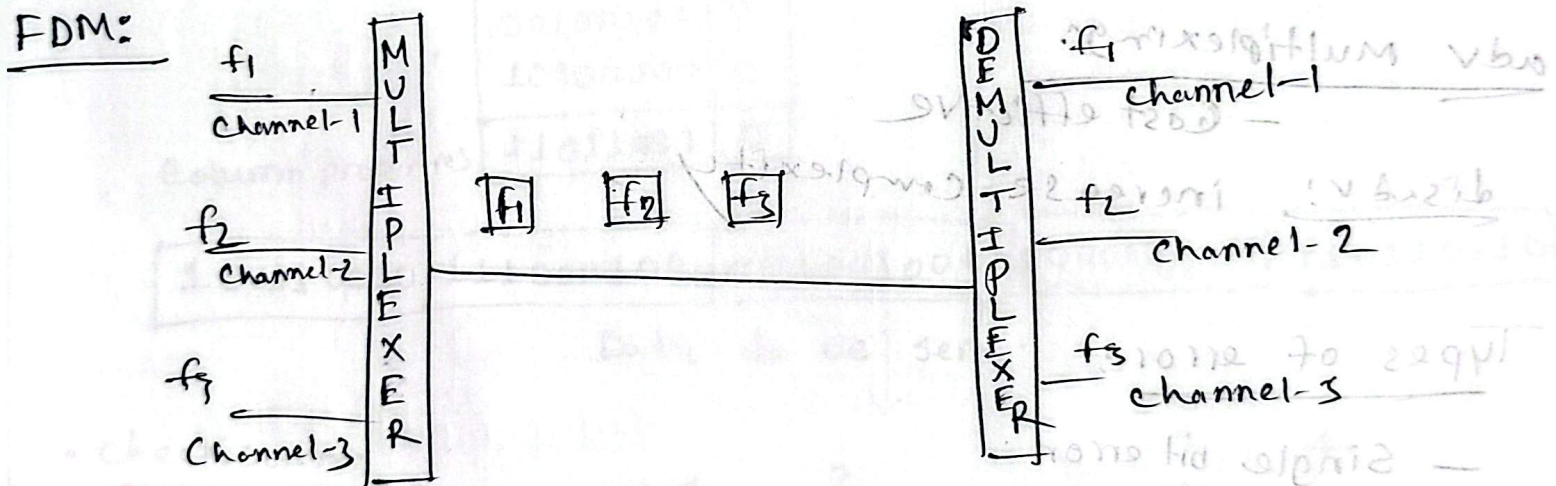
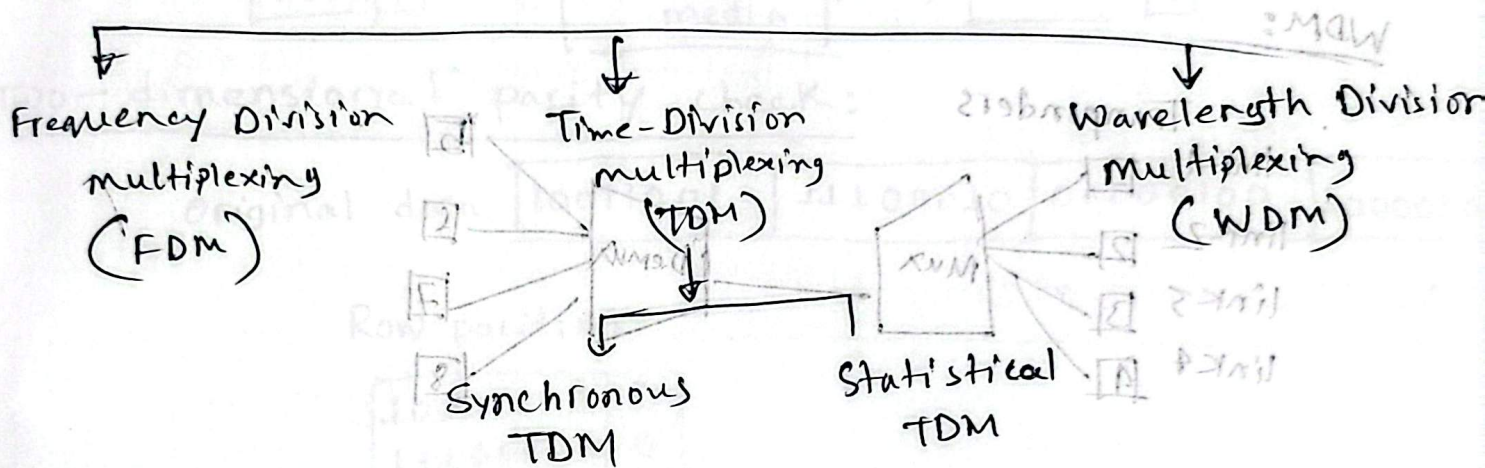
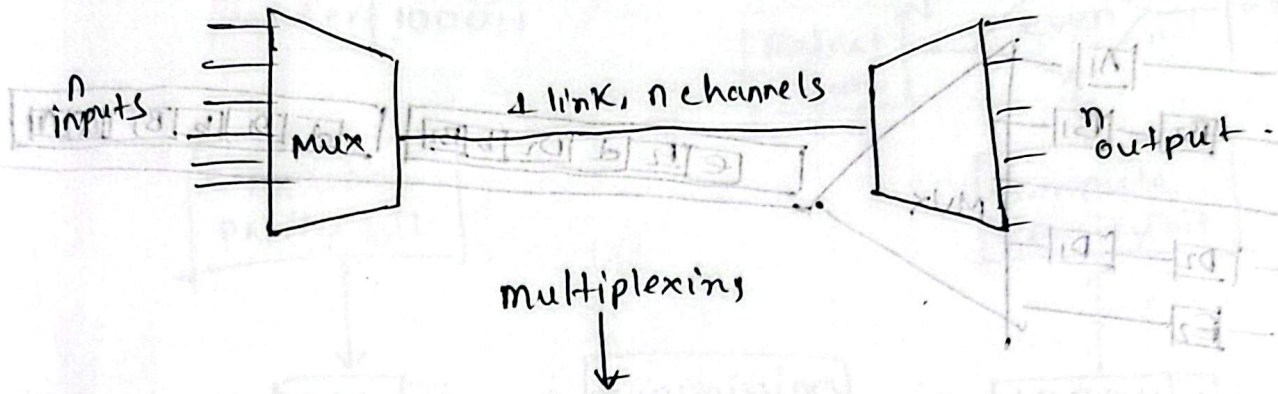


Modulations the process by which data is converted into electrical/digital signal varies continuously like a sine wave for transferring that signal over a medium is called modulation (digital-anal)

Demodulations the process of extracting data from the transmitted signal is called demodulation, (analog-digital)

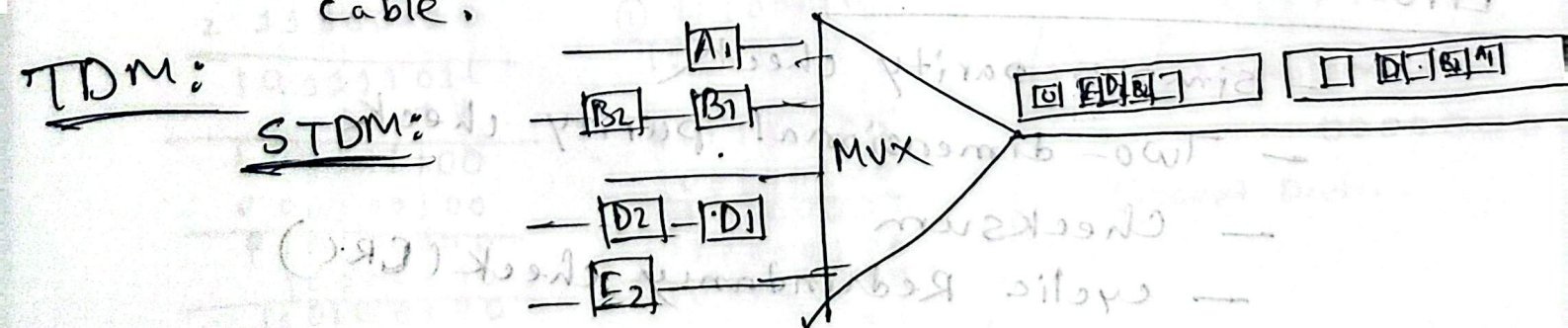


Multiplexing

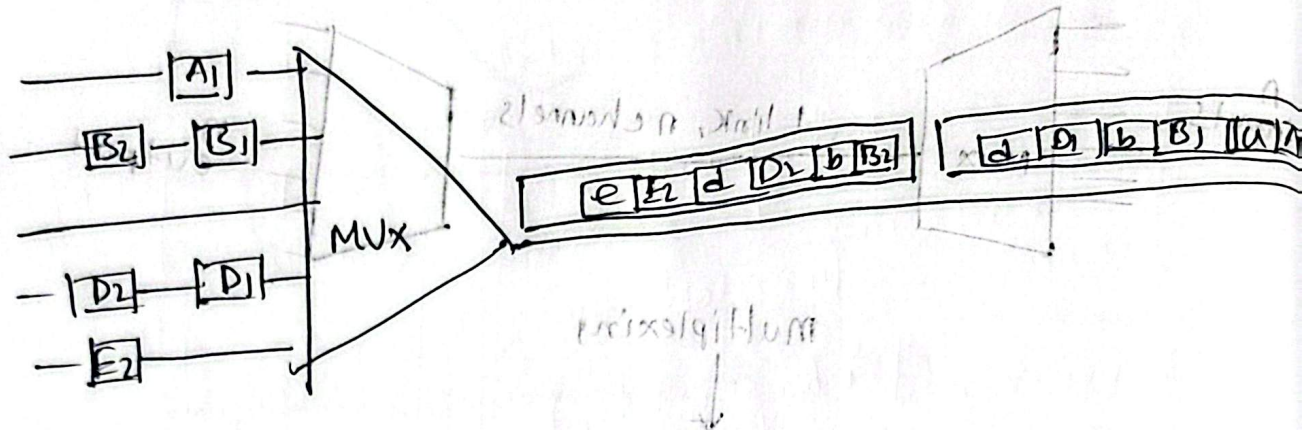


adv: simple and easy

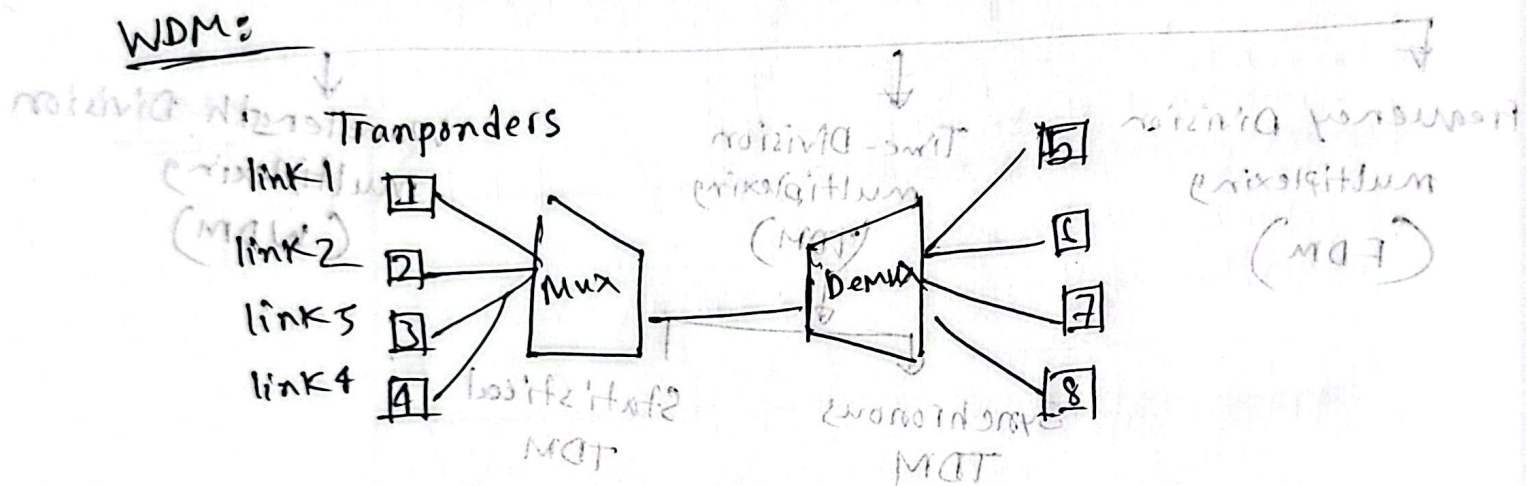
disadv: it can not utilize the full capacity of the cable.



Statistic TDM:



WDM:



adv Multiplexing:

— Cost effective

disadv: increase complexity

Types of error:

- Single bit error:
- Multiple " "

— Burst error: Several consecutive bits are flipped

Error detection method:

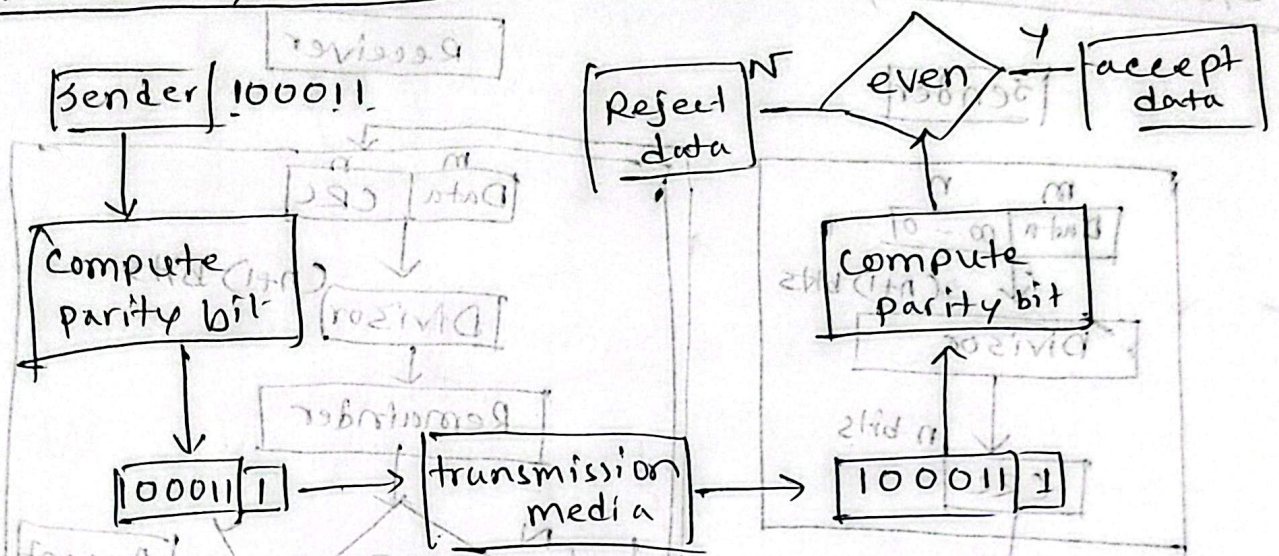
[1] [0] [1] [1] simple parity check

— Two-dimensional parity check

— Checksum

— cyclic Redundancy check (CRC)

• Simple parity check:



• Two-dimensional parity check:

Original data: 10011001 11100010 00100100 10000100

Row parities

10011001	0
11100010	0
00100100	0
10000100	0
11011011	0

Column parities

Data to be sent: 100110010 111000100 001001000 100001000 110110110

Data to be sent.

• Checksum:

Original Data

K=4, m=8

Sender

```

1 10011001
2 11100010
-----
10111101
+1
-----
101111100
000100100
-----
010100000
10000100
-----
100100100
+1
-----
000100101
    
```

Receiver

```

① 10011001
② 11100010
-----
101111011
+1
-----
001111100
00100100
-----
101000000
10000100
-----
100100100
+1
-----
00100101
    
```

```

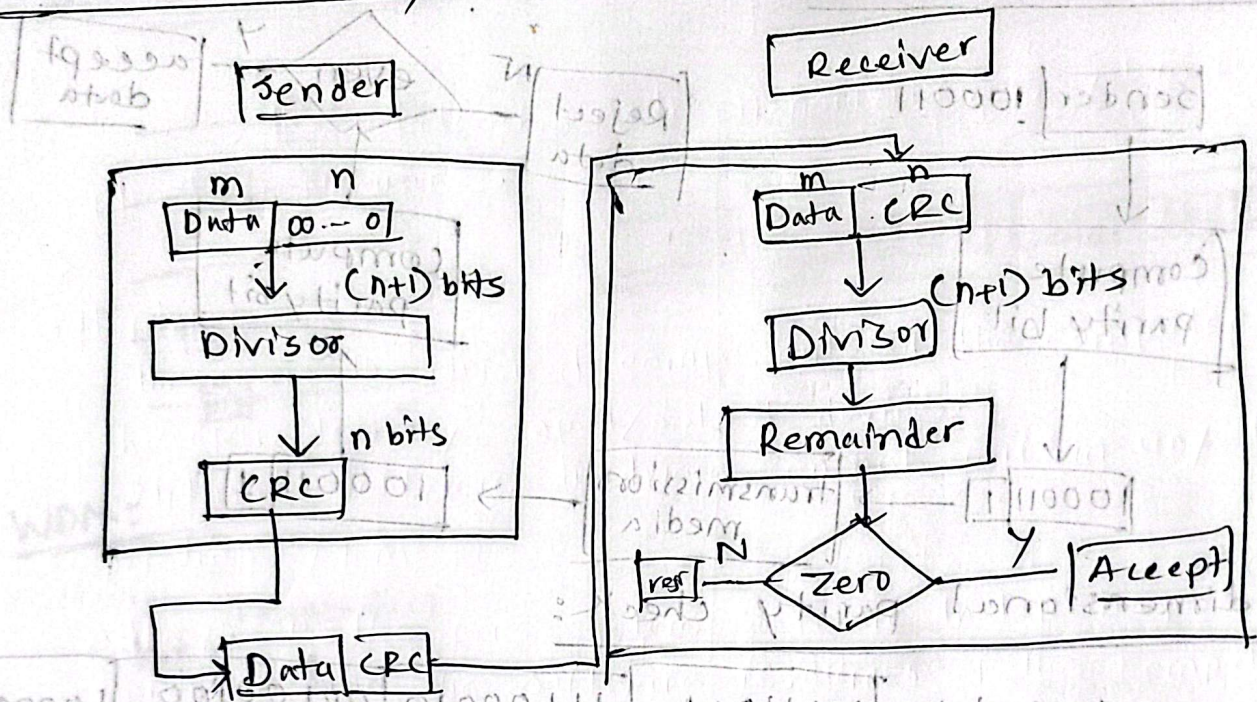
00100101
11011010
-----
11111111
    
```

complement

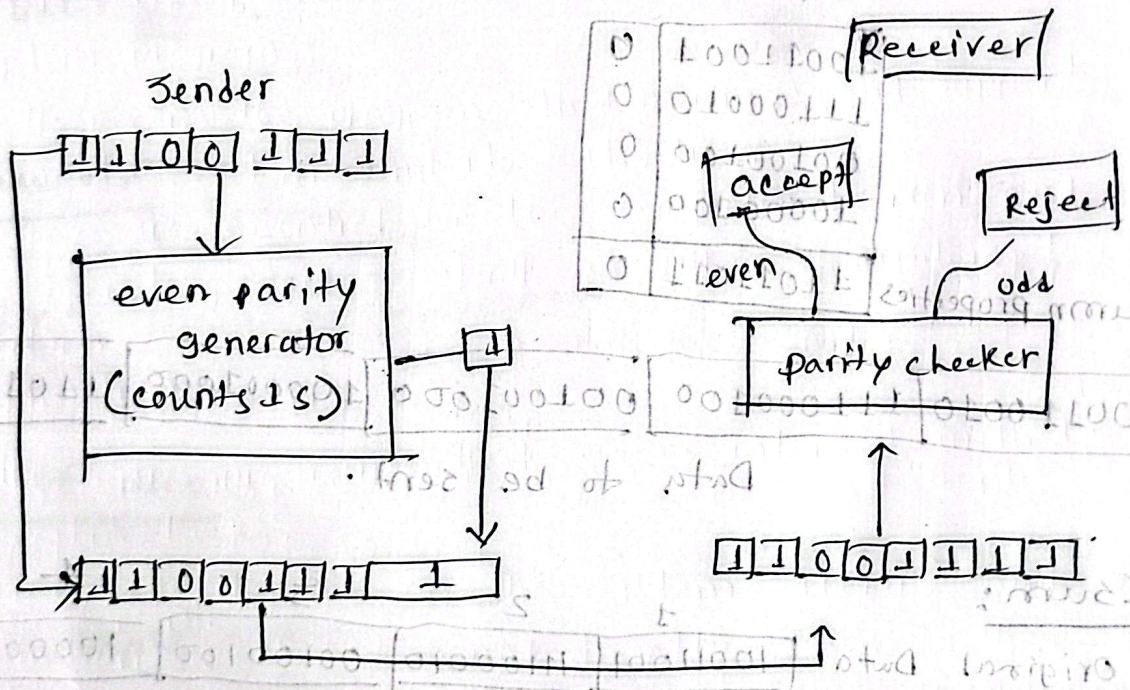
00000000

Accept Data.

Cycle Redundancy Check:



Vertical Redundancy Check (VRC) or parity check:



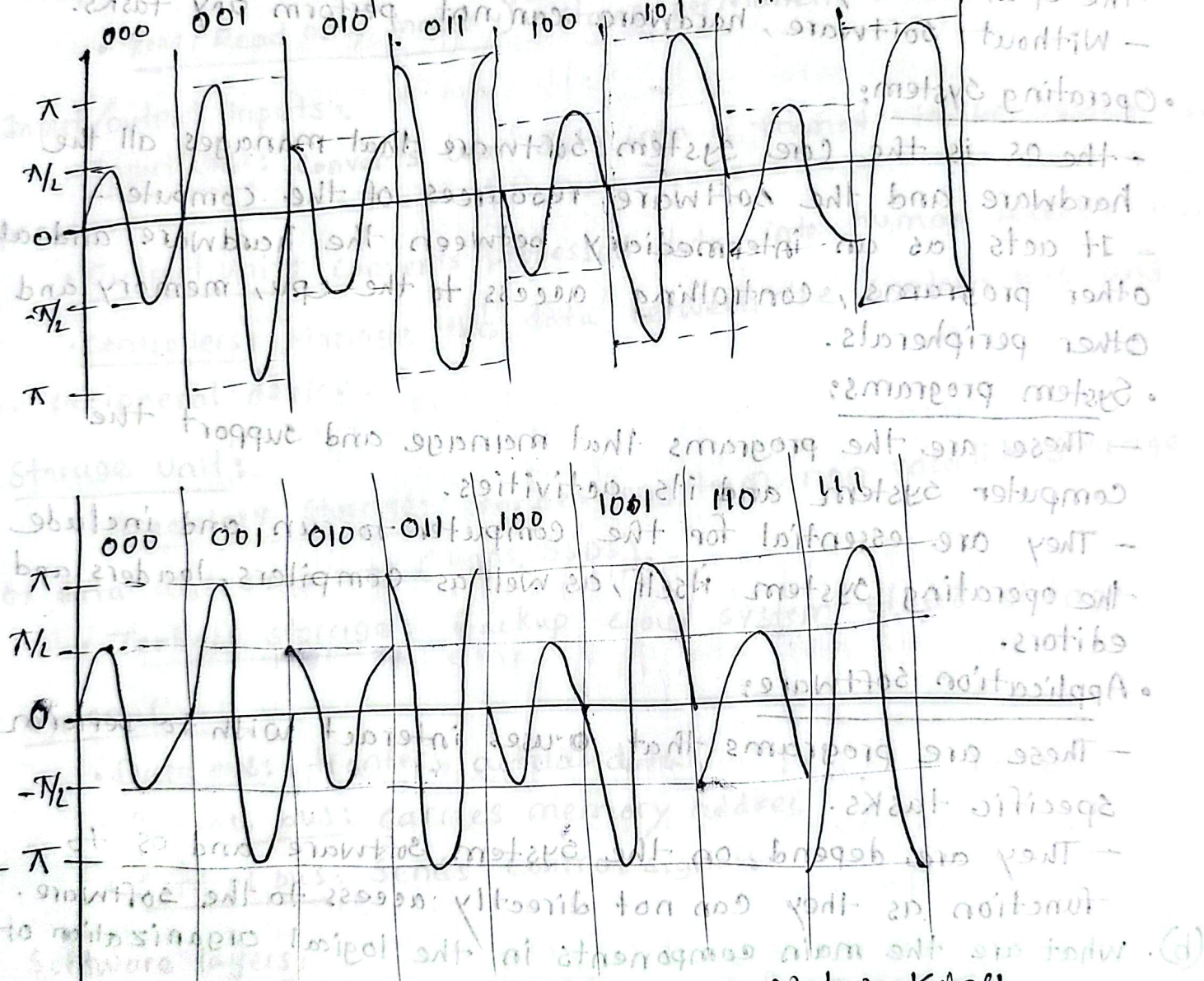
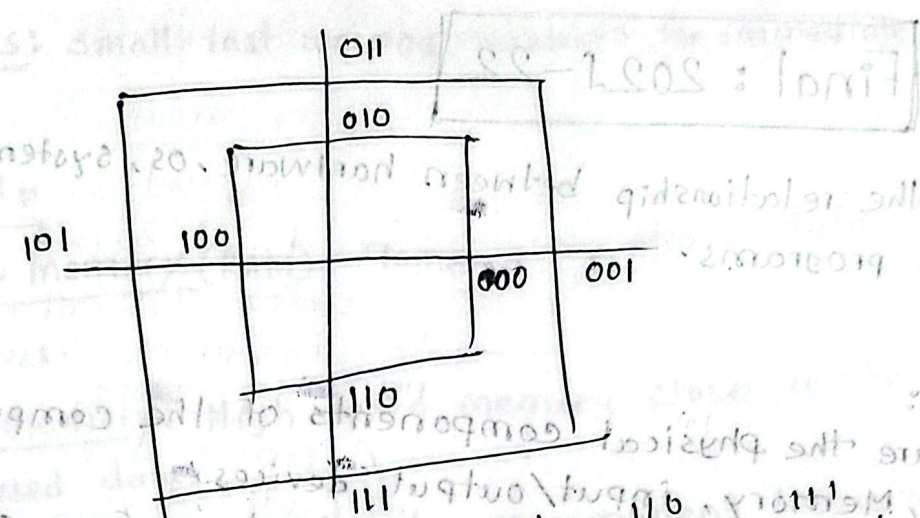
Receiver

1	0011001
0	1100100
1	1011101
1	0111100
0	0100100
1	1010000
0	1000100
1	00100100
1	10100100
0	0100100
1	10100100

Sender

1	0011001
0	1100100
1	1011101
1	0111100
0	0100100
1	1010000
0	1000100
1	00100100
1	10100100
0	0100100
1	10100100

Registers: small fast memory units for temporary storage of data and instructions.



Data communication and Networking

BE HROUZ A. FOROUZAN

Control Unit (CU): Directs operations of the processor and co-ordinates between components. Arithmetic Logic Unit (ALU): performs mathematical and logical operations.