

Mid 2 (Set B)

1. (a)

[Marks 5]

(i) $(x^3 + x^v + x + 1) + (x^4 + x^v + x + 1)$

Here,

$$P_1 = x^3 + x^v + x + 1$$

$$P_2 = x^4 + x^v + x + 1$$

Binary form of P_1 is = 01111

Binary " " P_2 is = 10111

addition of these polynomial = 11000

$\therefore$ Polynomial addition (XOR operation) is = 11000. Ans
 $= (x^4 + x^3) \rightarrow$

(ii) $(x^3 + x^v + x + 1) / (x^v + 1)$

Here,

$$P_1 = x^3 + x^v + x + 1, \text{ Binary form is } = 1111$$

$$P_2 = x^v + 1, \text{ " " is } = 101$$

$$\begin{array}{r}
 110 \rightarrow \text{Quotient} \\
 101 \overline{) 1111} \\
 \underline{101} \\
 01011 \\
 \underline{101} \\
 000 \\
 \underline{000} \\
 000 \rightarrow \text{Remainder}
 \end{array}$$

So, answer is $x+1$.

another Method,

$$\begin{array}{r}
 x+1 \xrightarrow{\hspace{1cm}} \text{answer.} \\
 x^v+1 \overline{) x^3+x^2+x+1} \\
 \underline{x^3 + x} \\
 x^2+1 \\
 \underline{x^2+1} \\
 0 \xrightarrow{\hspace{1cm}} \text{Remainder}
 \end{array}$$

(b) Given,
CRC-16 ($x^{16} + x^{12} + x^5 + 1$).

Now ~~converting~~ converting this polynomial equivalent to its binary form

$$= x^{16} + 0x^{15} + 0x^{14} + 0x^{13} + 0x^{12} + 0x^{11} + 0x^{10} + 0x^9 + 0x^8 + 0x^7 + 0x^6 + 0x^5 + 0x^4 + 0x^3 + 0x^2 + 0x + 1$$

$$= 1000100000001000001 \quad (\text{Ans})$$

(2) Given,

Data word = 5 bits

code word = 8 bits.

Os need to make dividend = 000 (3bits)

Size of remainder is = 3 bits

Size of divisor is = 5-1 = 4 bits.

3.

Given,

dataword = 101001111

divisor = 10111

Now, redundant bit = divisor - 1 = 5 - 1 = 4 bits of 0.

add 4 bits of 0 with dataword and perform division,

Sender $\Rightarrow$

	100110111
10111	1010011110000
	10111
	000111
	000000
	001111
	00000
	11111
	10111
	010001
	10111
	001100
	00000
	011000
	10111
	011110
	10111
	010010
	10111
	00101

Codeword = data + remainder

00101 $\rightarrow$ Remainder

$\therefore$ Codeword = 1010011110101

Receiver $\Rightarrow$

add remainder with dataword and perform division again.

	100110111
10111	1010011110101
	10111
	000111
	00000
	001111
	00000
	011111
	10111
	010001
	10111
	001100
	00000
	011001
	10111
	011100
	10111
	010111
	10111
	00000

Reminder $\rightarrow$ 00000

As remainder is 0 at the receiver site so, there is no error in codeword and accept it.

If the remainder will not zero then the data will not accept and error detected.

4. In noisy channel, Go-back-N and Stop-and-Wait ARQ faces some problems that are given below,

① Go-Back-N ARQ:

1. If a frame is lost, the sender goes back and resend that frame.
2. But, when the sender resends the lost frame, it also resends all subsequent frame, even if they were received correctly.
3. These cause waste of Bandwidth and extra delay.

② Stop-and-Wait ARQ:

1. Wait for acknowledgment after each frame.
2. Very inefficient for long-distance link.
3. Huge waiting time during long-distance and high-speed links transmission.

To overcome those problem, there is a solution named Selective Repeat ARQ. The way it solve those problem given below:

1. Selective Repeat ARQ retransmits only the specific frames that ~~was~~ were lost.
2. It uses a buffer at both sender and receiver to store out-of-order frame.
3. This improves efficiency in noisy channel and better utilization of bandwidth.

Working Procedure:

Sender:

1. Sends multiple frames at a time
2. Maintain a window of frames that it can used
3. If it receives no ACK within timeout, it retransmit only the missing frame.

Receiver:

1. Accepts out-of-order frames and store them in buffer
2. Sends ACK for each correctly receive frame.

Here is an diagram and expt example given below:

Example:

step 1:

Sender sends frame 0

step 2:

Sender sends frame 1

Receiver sends ACK
for ~~frame~~ frame 2

step 3:

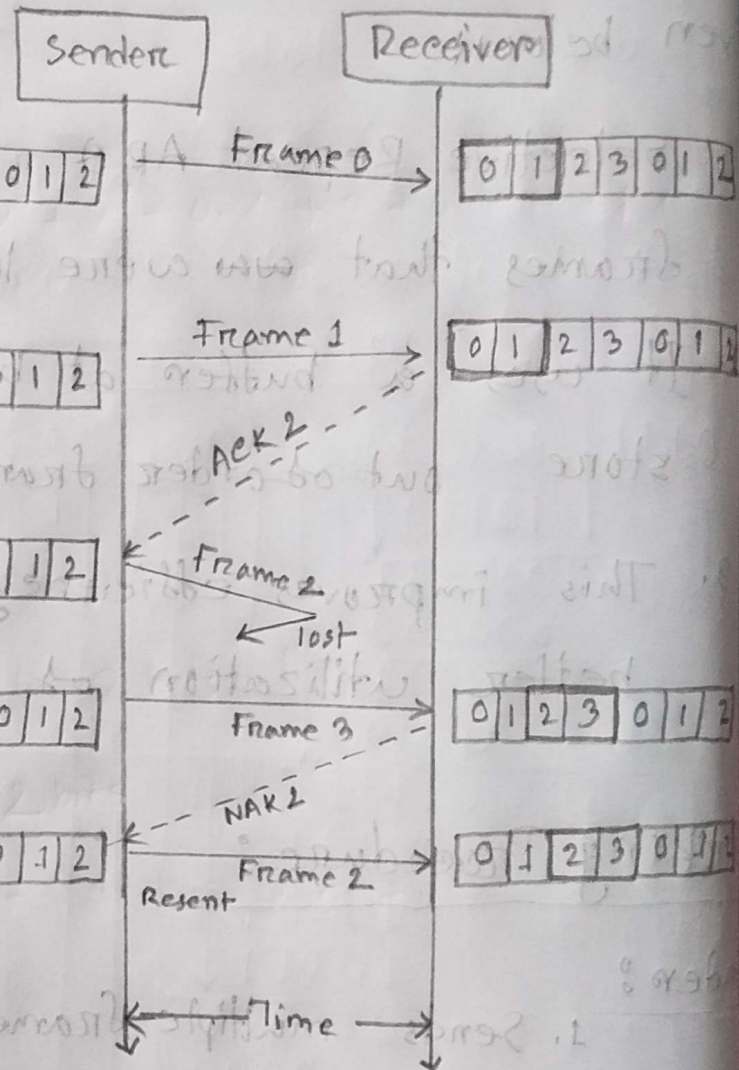
Sender sends frame 2
but it lost

step 4:

Sender sends frame 3

~~step 5~~ Receiver sends
NAK for resent frame 2

step 5: sender ~~resend~~ resent frame 2.



— Diagram —

Mid 2 (Set A)

1) a

① ~~$x^5 + x^3 + x + 1$~~ $\cdot (x^5 + x^3 + x + 1) + (x^4 + x^2 + x + 1)$

$P_1 = x^5 + x^3 + x + 1$, Binary = 101011

$P_2 = x^4 + x^2 + x + 1$, Binary = 010111

111100

$\therefore$ Answer is $= 111100 = x^5 + x^4 + x^3 + x^2$

②

~~$x^3 + 1$~~

$(x^4 + x^2 + x + 1) / (x^3 + 1)$

$x^3 + 1$	$ \begin{array}{r} x^4 + x^2 + x + 1 \\ \underline{x^3 } \\ x^1 + 1 \end{array} $	← Quotient
	$ \begin{array}{r} x^4 + x^2 + x + 1 \\ \underline{x^3 } \\ x^1 + 1 \end{array} $	← Remainder.

$\therefore$ Answer is x and remainder is $x^1 + 1$

Another Method:

1001	$ \begin{array}{r} 1 \\ \underline{10111} \\ 1001 \end{array} $	← Quotient
	$ \begin{array}{r} 10111 \\ \underline{1001} \\ 00101 \end{array} $	← Remainder

$\therefore$ Answer is x and remainder is $x^1 + 1$

(b)

$$\text{CRC-10 } (x^{10} + x^9 + x^8 + x^7 + x^6 + 1)$$

Convert into binary =

$$x^{10} + x^9 + 0 \cdot x^8 + 0 \cdot x^7 + 0 \cdot x^6 + x^5 + x^4 + 0 \cdot x^3 + x^2 + 0 \cdot x + 1$$

$$\Rightarrow 11000110101$$

2.

data word = 6

code word = 8

to make dividend, add = 2 bits of 0

Remainder size = 2 bits

divisor size is = $6 - 1 = 5$ bits.

$$\frac{1 + x + x^2 + x^3 + x^4}{1 + x}$$

Method 1

Method 2

$$\begin{array}{r} 11101 \\ 10111 \\ \hline 1001 \end{array}$$

$$\begin{array}{r} 1001 \\ 10101 \\ \hline 00101 \end{array}$$

Remainder

3. Given, dataword = 111001011
divisor = 11101

redundent bits = $5-1 = 4$ bits of 0.

Sender →

100010101
11101 | 11100101110000
11101 ↓
000011
00000 ↓
000110
00000 ↓
001101
00000 ↓
011011
11101 ↓
001100
00000 ↓
11000
11101 ↓
001010
00000 ↓
10100
11101 ↓
01001 → Remainder.

Codeword = 1110010111001

Receiver:

100010101
11101 | 1110010111001
11101 ↓
000011
00000 ↓
000110
00000 ↓
001101
00000 ↓
011011
11101 ↓
001101
00000 ↓
011010
11101 ↓
001110
00000 ↓
011101
11101 ↓
00000
Remainder → 00000

As the remainder is 0 at the receiver side, so there is no error and data is correctly received.

If the remainder will not 0 then there will be an error and data wouldn't accept by receiver.